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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/872,435

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Stephen L. Bade

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EXAMINER

LUU, CUONG V

ART UNIT

PAPER NUMBER

2128

NOTIFICATION DATE

DELIVERY MODE

08/13/2009

ELECTRONIC

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

mserna@beverlaw.com  
creddick@beverlaw.com

**Supplemental  
Notice of Allowability**

**Application No.**

09/872,435

**Examiner**

Cuong V. Luu

**Applicant(s)**

BADE ET AL.

**Art Unit**

2128

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 3/12/2007.
2. ☒ The allowed claim(s) is/are 37-78 and 89.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

**Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).**
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |                                                                                                            |                                                                                        |
|------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)                                           | 5. <input type="checkbox"/> Notice of Informal Patent Application                      |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                       | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),<br>Paper No./Mail Date _____    | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|                                                                                                            | 9. <input type="checkbox"/> Other _____.                                               |

### **DETAILED ACTION**

This Supplemental Allowance After Examination is to make a correction to a discrepancy to the previous Allowance After Examination. The discrepancy is that claim 73 was mistakenly indicated canceled. Therefore, claim 73 is included in this Supplemental Allowance After Examination for allowance.

Claims 37-78 and 89 are pending. Claims 1-36, 79-88 have been canceled. Claims 37-78 and 89 have been examined. Claims 37-78 and 89 have been allowed.

### ***Allowable Subject Matter***

**Claims 37-78 and 89 are allowed. The following is an examiner's statement of reasons for allowance:**

1. As per claim 37, the prior arts of record do not teach generating said FSM simulation in combination with comprising applying a design language having at least one graphical symbol and adapted to form a finite state machine representation of electronic hardware, each graphical symbol of the design language having a graphical portion and a user-definable textual portion defining the behavior of the graphical symbol and a virtual test-bench for selecting signals or variables associated with the FSM as described in paragraph 0098 on pages 31-32 and paragraph 0101 on page 34 of the specification.
2. As per claim 37, the prior arts of record do not teach generating a virtual hardware component that is a finite state machine (FSM) in combination with comprising applying a design language having at least one graphical symbol and adapted to form an FSM

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representation of electronic hardware as described in paragraph 0098 on pages 31-32 of the specification.

3. As per claim 64, the prior arts of record do not teach generating a virtual hardware component that is a finite state machine (FSM) in combination with comprising applying a design language having at least one graphical symbol and adapted to form an FSM representation of electronic hardware and a virtual test-bench for selecting signals or variables associated with the FSM as described in paragraph 0098 on pages 31-32 and paragraph 0101 on page 34 of the specification.
4. As per claim 68, the prior arts of record do not teach a finite state machine (FSM) in combination with representation of a hardware element within the embedded system, configured to couple memory read/write requests and interrupt signals and a virtual test-bench for selecting signals or variables associated with the FSM as described in paragraph 0098 on pages 31-32 and paragraph 0101 on page 34 of the specification.
5. As per claim 74, the prior arts of record do not teach generating said FSM simulation in combination with comprising applying a design language having at least one graphical symbol and adapted to form a finite state machine representation of electronic hardware, each graphical symbol of the design language having a graphical portion and a user-definable textual portion defining the behavior of the graphical symbol and a virtual test-bench for selecting signals or variables associated with the FSM as described in paragraph 0098 on pages 31-32 and paragraph 0101 on page 34 of the specification.

**Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."**

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cuong V. Luu whose telephone number is 571-272-8572. The examiner can normally be reached on Monday-Friday 8:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamini Shah, can be reached on 571-272-2279. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. An inquiry of a general nature or relating to the status of this application should be directed to the TC2100 Group receptionist: 571-272-2100.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Kamini S Shah/

Supervisory Patent Examiner, Art Unit 2128

/Cuong V Luu/

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